SEM825

Semiconductors – Integrated Circuits

							Price Each		
	Mftr.	Pins	Description	Mftrs. List No.	Order Code	1+	10+	100+	250+
86E44	ZILOG	40	8-bit Microcontroller, 16Kb OTP PROM, 236 bytes RAM, 32 I/O lines, with selectable oscillator – 12MHz	Z86E44-12PSC	311-3012				
86E61	ZILOG	40	8-bit Microcontroller, 16Kb OTP PROM, 236 bytes RAM, 32 I/O lines, with crystal oscillator – 16MHz		311-3024				
86E63	ZILOG	40	8-bit Microcontroller, 32Kb OTP PROM, 236 bytes RAM, 32 I/O lines, with crystal oscillator – 16MHz		311-3036				
86E83	ZILOG	28	8-bit Microcontroller, 4Kb OTP PROM, 236 bytes RAM, 21 I/O lines, with crystal oscillator – 16MHz		311-3073				
Z8			orystal osolilator Town iz	200200 101 00	011 0070				
80180 86733	ZILOG ZILOG		8-bit CMOS CPU, 10MHz Clock (enhanced Z80)		311-2718				
			WDT, Comparators - 12MHz	Z86733-12PSC	311-2779				
86743	ZILOG	40	8-bit CMOS CPU, 8Kb OTP PROM, 236 bytes RAM, 32 I/O, 8-bit Timers + WDT, Comparators - 12MHz	Z86743-12PSC	311-2780				
Zilog Technical Library CD-ROM					316-7446				
7ilon Discrete 786 Microcontrollers Databook Order Code 504-750 each									

Z8S180 Evaluation Kit

Zilog

The evaluation kit consists of an assembled circuit board, software and documentation to support hardware and software development for the Z8S180 device. Z8S180/ESCC evaluation board ● 18.432MHz crystal

Socketed 64K EPROM

 RS-232 interface and cable Expansion header

Reset switch

Sample code is supplied to illustrate Z8S180 in a variety of communications applications.

The supplied cross-assembler and link/loader package allows full assembly language programming support. On-board EPROM resident debug monitor program allows executable code to be downloaded and debugged.

Supplied with a US power lead. UK (439-319), EU (210-699) leads available separately.

Z8S18000ZCO Evaluation Kit......Order Code 311-2720

each

Z8S180 ZiLOG

An enhanced model of the Z80180 with faster execute speeds, power saving modes and EMI noise reduction.

 Core compatible with Z80 CPU Features include

Z8S180 micro

Extended instructions Low power down modes Clocked serial I/O port

 On-chip interrupt controller On-chip oscillator/generator 2 16-bit timers 2 UARTs

Price Each Pins Description Mftrs. List No. **Order Code** 10+ 100+ 8-bit COMS CPU, 20MHz (enhanced Z80180) Z8S180-20PSC1960 311-2755 8-bit COMS CPU, 20MHz (enhanced Z80180) 78S180-20PSC 311-2743 8-bit COMS CPU, 33MHz (enhanced Z80180). Z8S180-33VSC MD 311-2767

Z8S180 Emulator ZiLOG

The Z8S180E Emulator provides the hardware, software and documentation necessary to evaluate, design and develop using the Z8S180 MPU.

It provides a learning environment for the Z80/Z8S180 architectures and an emulator for designs.

The kit contains a monitor program which provides downloading and runs programs with or without breakpoints, displaying and filing memory locations, comparing contents of memory and reading/writing from the I/O ports.

Board assembly includes:

Z8S180 MPU

18 432 crystal

8K 8 SRAM

Supplied with a US power lead, UK (439-319), EU (210-699) leads available separately.

Z8S180 and cross assembler utility disks

8K 8 EPROM (contains debug monitor)

FPGA (controls emulation, ROM and RAM decoding)

 68 PLCC emulation adapter Manual, databook and user guide RS-232 line driver/receiver

Reset buttons

Z8S18000ZEM EmulatorOrder Code 311-2731

Z80S183

Kit also includes:

Z8S180

78S180

78S180

The Z80S183 is a highly integrated general purpose mixed signal microcontroller. It contains the full features of the Z80S180 device, with an enhanced structure which offers:

32-bits of general purpose I/O

A/D converter with 8 multiplexed Inputs

33MHz speed

D/A converter

2 x enhanced UARTs 2 x 16-hit timers

Low power PLL oscillator

5V & 3.3V operation

SFM915

100+

New

each

Pins Description

8-bit Mixed signal CPU, 33MHz (Enhanced Z80S180)

Mftrs List No. Z80S183AZ033SCR4567

Order Code

Price Each 1+ 10+

Z80S183 Development Tools

The Z80S183 Evaluation Kit is a general purpose platform that allows the user to evaluate the capabilities of the Z80S183 integrate controller. It can be operated in a stand-alone mode or attached to a PC via the ZPAK emulator. Features of the Evaluation Kit include:



Analogue loop-back for DAC/ADC

Reset & NMI Pushbuttons • 128K x 8, 32 pin SRAM on board RS232 Serial port

 Up to 33MHz operation Extension Busses available 32 pin ROM socket

 0.1R resistor site for 5V analogue and digital power measurement



Note: requires a 9V dc power input

The ZPAK Emulator provides full emulation for ZILOG processors equipped with on-chip support hardware. With the included software, the user can write, edit and debug code. The emulator can be used in conjunction with the Z80S183 Evaluation board. Key features include:

- 40MHz processor providing Serial Comms to PC, Debug
 Manuals and documentation monitor. I/O mapped ZDI control register interface
 128Kb FLASH memory Connection to target board via 6-pin header
 - 128Kb FLASH memory 512Kb SRAM memory
- 7II OG Developer Studio software
- PC serial Communication leads

Order Code 344-7893

Z80S183 Evaluation Kit Z80S183 Emulator/Debug Module

Price Each Mftrs List No. Z80S1830100ZC0 ZDI123200ZPK